Overview:

This course (CCP152-EL) is a skill-building programming course that provides you with an understanding of Studio 5000 Logix Designer® function block diagrams and terminology. You will learn how to perform parameter modifications to individual function block instructions, as well as create and develop function block diagram programs and routines. You will also gain experience with a variety of function block instructions, including PIDE and add-on instructions.

Objectives:

- Creating a Function Block Diagram
- Programming Logical Function Block Instructions
- Programming Timer and Counter Function Block Instructions
- Programming Analog Function Block Instructions
- Programming Device Driver Function Block Instructions
- Selecting Timing Modes in a Function Block Instruction
- Programming a Totalizer Function Block Instruction
- Programming and Monitoring an RMPS (Ramp/Soak) Function Block Instruction
- Controlling Program Flow Using Function Block Instructions
- Programming a PID Loop Using Function Block Diagram
- Tuning a PID Loop Using Active X Controls
- Developing an Add-On Instruction in Function Block Diagram

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Who Should Attend:

Individuals who are responsible for developing, debugging, and programming Logix 5000 controllers using the Studio 5000 Logix Designer application with function block diagrams should attend this course.

Prerequisites:

To successfully complete this course, the following prerequisites are required:

- Completion of the Studio 5000 Logix Designer Level 3: Project Development course (Course No. CCP143)
- Understanding of basic measurement and control theory, including basic loop control

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